

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Gerrit Jan Hemink		
Assignee:	SanDisk Corporation		
Title:	Self-Boosting System for Flash Memory Cells		
Patent No.:	7,161,833 B2	Issue Date:	January 9, 2007
Application No.:	10/774,014	Filing Date:	February 6, 2004
Examiner:	Tran, Michael Thanh	Group Art Unit:	2827
Docket No.:	SNDK.327US0	Conf. No.:	8419

Attn: Certificate of Correction Branch Of The Patent Issue Division
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION

Sir:

Please enter the enclosed Certificate of Correction (PTO Form 1050) in the above patent.

The errors sought to be corrected were made by the Patent and Trademark Office. Thus, no fee is required for the Certificate of Correction pursuant to 37 CFR §1.322.

Please correct the following errors made by the USPTO:

Column 20, line 11, claim 1; before "or more charge storage", delete "one" and insert --three--. (See Amendment filed December 28, 2005).

Column 20, line 15, claim 1; delete "boosting". (See Amendment filed December 28, 2005).

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Patent No.: 7,161,833 B2

Column 20, line 25, claim 2; delete “boosting”. (See Amendment filed December 28, 2005).

Column 20, line 31, claim 4; delete “potential” and insert –potential(s)--. (See original claim 4 of the Specification filed February 6, 2004).

Column 20, line 32, claim 4; delete “(s)”. (See original claim 4 of the Specification filed February 6, 2004).

Column 20, line 46, claim 8; after “one select transistor”, insert --in the first string--. (See Amendment filed December 28, 2005).

Column 20, line 47, claim 8; before “the first voltage”, insert --wherein--. (See Amendment filed December 28, 2005).

Column 21, line 15; claim 13; before “or more”, delete “one” and insert --three--. (See Amendment filed December 28, 2005).

Column 21, line 19; claim 13; delete “boosting”. (See Amendment filed December 28, 2005).

Column 21, line 50, claim 14; after “transistors”, delete “including” and insert --and a voltage level to--. (See Amendment filed December 28, 2005).

Column 21, line 52, claim 14; delete “boosting”. (See Amendment filed December 28, 2005).

Column 21, line 62, claim 15; delete “boosting”. (See Amendment filed December 28, 2005).

Column 21, line 64, claim 15; delete “more”. (See Amendment filed December 28, 2005).

Column 21, line 65, claim 15; delete “than” and after “two” insert --or more--. (See Amendment filed December 28, 2005).

Column 22, line 11, claim 18; delete “third”. (See Amendment filed December 28, 2005).

Column 22, line 16, claim 19; delete “boosting”. (See Amendment filed December 28, 2005).

Column 22, line 17, claim 19; after “second”, delete “boosting”. (See Amendment filed May 16, 2006).

Column 22, line 28, claim 23; after “transistor”, insert --in the first string--. (See Amendment filed December 28, 2005).

Column 22, line 29, claim 23; after “and”, insert --wherein--. (See Amendment filed December 28, 2005).

Column 22, line 67, claim 28; after “second string”, insert --to a value or values closer to the program voltage to reduce program disturb--. (See Amendment filed December 28, 2005).

Column 23, line 1, claim 28; delete “boosting”. (See Amendment filed December 28, 2005).

Column 23, line 5, claim 28; delete “boosting”. (See Amendment filed December 28, 2005).

Column 23, line 12, claim 29; delete “boosting”. (See Amendment filed December 28, 2005).

Column 23, line 16, claim 30; delete “28” and insert --29-- and delete “boosting”. (See Amendment filed December 28, 2005).

Column 23, line 18, claim 30; before “such that”, insert --is--. (See Amendment filed December 28, 2005).

Column 23, line 27, claim 33; delete “boosting”. (See Amendment filed December 28, 2005).

Column 23, line 40, claim 34; delete “does” and insert --do--. (See claim 35 in Amendment filed December 28, 2005).

Column 23, line 44, claim 35; delete “potential” and insert --potential(s)--. (See original claim 36 of the Specification filed February 6, 2004).

Column 23, line 44, claim 35; delete “(s)”. (See original claim 36 of the Specification filed February 6, 2004).

Column 24, line 16, claim 39; delete “one” and insert --two--. (See claim 52 in Amendment filed December 28, 2005).

Column 24, line 17, claim 39; before “in”, delete “transistor” and insert --transistors--. (See claim 52 in Amendment filed December 28, 2005).

Column 24, line 23, claim 39; after “string” insert --to a value or values closer to the program voltage to reduce program disturb--. (See claim 52 in Amendment filed December 28, 2005).

Column 24, line 24, claim 39; delete “boosting”. (See claim 52 in Amendment filed December 28, 2005).

Column 24, line 25, claim 39; after “at least”, delete “twocharge” and insert --two charge--. (See original claim 52 in the Specification filed February 6, 2004).

Column 24, line 28, claim 39; delete “boosting”. (See claim 52 in Amendment filed December 28, 2005).

Column 24, line 35, claim 40; delete “boosting”. (See claim 55 in Amendment filed December 28, 2005).

Column 24, line 47, claim 43; before “volts”, delete “2” and insert --3--. (See claim 58 in Amendment filed May 16, 2006).

Column 24, line 58, claim 45; after “transistor”, delete “on” and insert --in--. (See claim 60 in Amendment filed December 28, 2005).

Column 24, line 65, claim 45; delete “boosting”. (See claim 60 in Amendment filed May 16, 2006).

Column 25, line 9, claim 45; delete “boosting”. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 13, claim 45; delete “boosting”. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 15, claim 45; delete “a selected” and insert --the first--. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 17, claim 45; delete “a selected” and insert --the first--. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 18, claim 45; before “at least one”, delete “the” and insert --such--. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 18, claim 45; after “at least one”, delete “of the second boosting”. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 18, claim 45; delete “level(s)” and insert --level--. (See claim 60 in Amendment filed December 28, 2005).

Column 25, line 22, claim 46; delete “boosting”. (See claim 63 in Amendment filed December 28, 2005).

Column 25, line 47, claim 51; after “electrical” delete “potential” and insert --potential(s)--. (See original claim 68 in the Specification filed February 6, 2004).

Column 25, line 48, claim 51; delete “(s)”. (See original claim 68 in the Specification filed February 6, 2004).

Column 26, line 27, claim 58; delete “one” and insert --two--. (See claim 71 in the Amendment filed December 28, 2005).

Column 26, line 45, claim 58; delete “and the bit line connected to the”. (See claim 71 in the Amendment filed December 28, 2005).

Column 26, line 46, claim 58; delete “second string”. (See claim 71 in the Amendment filed December 28, 2005).

Column 26, line 48, claim 58; delete “boost-”. (See claim 71 in the Amendment filed December 28, 2005).

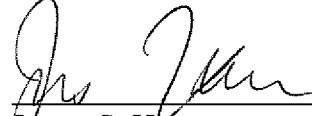
Column 26, line 49, claim 58; delete “ing”. (See claim 71 in the Amendment filed December 28, 2005).

Column 26, line 55, claim 59; after “coupled to” insert --it or--. (See claim 74 in the Amendment filed December 28, 2005).

Please direct all inquiries concerning this request to the undersigned attorney at (415) 276-6500.

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Respectfully submitted,


James S. Hsue

Reg. No. 29,545

4/13/09
Date

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,161,833 B2
APPLICATION NO.: 10/774,014
ISSUE DATE : Jan. 9, 2007
INVENTOR(S) : Gerrit Jan Hemink

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 20, line 11, claim 1; before "or more charge storage", delete "one" and insert --three--.
Column 20, line 15, claim 1; delete "boosting".
Column 20, line 25, claim 2; delete "boosting".
Column 20, line 31, claim 4; delete "potential" and insert --potential(s)--.
Column 20, line 32, claim 4; delete "(s)".
Column 20, line 46, claim 8; after "one select transistor", insert --in the first string--.
Column 20, line 47, claim 8; before "the first voltage", insert --wherein--.
Column 21, line 15; claim 13; before "or more", delete "one" and insert --three--.
Column 21, line 19; claim 13; delete "boosting".
Column 21, line 50, claim 14; after "transistors", delete "including" and insert --and a voltage level to--.
Column 21, line 52, claim 14; delete "boosting".
Column 21, line 62, claim 15; delete "boosting".
Column 21, line 64, claim 15; delete "more".
Column 21, line 65, claim 15; delete "than" and after "two" insert --or more--.
Column 22, line 11, claim 18; delete "third".
Column 22, line 16, claim 19; delete "boosting".
Column 22, line 17, claim 19; after "second", delete "boosting".
Column 22, line 28, claim 23; after "transistor", insert --in the first string--.
Column 22, line 29, claim 23; after "and", insert --wherein--.
Column 22, line 67, claim 28; after "second string", insert --to a value or values closer to the program voltage to reduce program disturb--.
Column 23, line 1, claim 28; delete "boosting".
Column 23, line 5, claim 28; delete "boosting".
Column 23, line 12, claim 29; delete "boosting".
Column 23, line 16, claim 30; delete "28" and insert --29--, and delete "boosting".
Column 23, line 18, claim 30; before "such that", insert --is--.
Column 23, line 27, claim 33; delete "boosting".

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Davis Wright Tremaine LLP
505 Montgomery Street, 8th Floor, San Francisco, CA 94111

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,161,833 B2

APPLICATION NO.: 10/774,014

ISSUE DATE : Jan. 9, 2007

INVENTOR(S) : Gerrit Jan Hemink

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 23, line 40, claim 34; delete "does" and insert --do--.

Column 23, line 44, claim 35; delete "potential" and insert --potential(s)--.

Column 23, line 44, claim 35; delete "(s)".

Column 24, line 16, claim 39; delete "one" and insert --two--.

Column 24, line 17, claim 39; before "in", delete "transistor" and insert --transistors--.

Column 24, line 23, claim 39; after "string" insert --to a value or values closer to the program voltage to reduce program disturb--.

Column 24, line 24, claim 39; delete "boosting".

Column 24, line 25, claim 39; after "at least", delete "twocharge" and insert --two charge--.

Column 24, line 28, claim 39; delete "boosting".

Column 24, line 35, claim 40; delete "boosting".

Column 24, line 47, claim 43; before "volts", delete "2" and insert --3--.

Column 24, line 58, claim 45; after "transistor", delete "on" and insert --in--.

Column 24, line 65, claim 45; delete "boosting".

Column 25, line 9, claim 45; delete "boosting".

Column 25, line 13, claim 45; delete "boosting".

Column 25, line 15, claim 45; delete "a selected" and insert --the first--.

Column 25, line 17, claim 45; delete "a selected" and insert --the first--.

Column 25, line 18, claim 45; before "at least one", delete "the" and insert --such--.

Column 25, line 18, claim 45; after "at least one", delete "of the second boosting".

Column 25, line 18, claim 45; delete "level(s)" and insert --level--.

Column 25, line 22, claim 46; delete "boosting".

Column 25, line 47, claim 51; after "electrical", delete "potential" and insert --potential(s)--.

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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APPLICATION NO.: 10/774,014

ISSUE DATE : Jan. 9, 2007

INVENTOR(S) : Gerrit Jan Hemink

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 25, line 48, claim 51; delete "(s)".

Column 26, line 27, claim 58; delete "one" and insert --two--.

Column 26, line 45, claim 58; delete "and the bit line connected to the".

Column 26, line 46, claim 58; delete "second string".

Column 26, line 48, claim 58; delete "boost-".

Column 26, line 49, claim 58; delete "ing".

Column 26, line 55, claim 59; after "coupled to" insert --it or--.

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